

G_S-2W & H_S-2W Series

2W, FIXED INPUT, 6000V ISOLATED & UNREGULATED SINGLE/DUAL OUTPUT DC-DC CONVERTER

multi-country patent protection **RoHS**

FEATURES

- High Efficiency Up To 80%
- SIP Package
- 6KVDC Isolation
- Low isolation capacitance
- Temperature Range: -40°C to +85°C
- Continuous Short Circuit Protection
- No Heatsink Required
- No External Component Required
- Internal SMD Construction
- Industry Standard Pinout
- RoHS Compliance

APPLICATIONS

The G_S-2W & H_S-2W Series are specially designed for applications where a group of polar power supplies are isolated from the input power supply in a distributed power supply system on a circuit board.

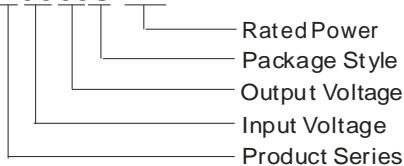
These products apply to:

- Where the voltage of the input power supply is fixed (voltage variation $\leq \pm 10\%$);
- Where isolation is necessary between input and output (isolation voltage $\leq 6000\text{VDC}$);
- Where the regulation of the output voltage and the output ripple noise are not demanding.

Such as: purely digital circuits, ordinary low frequency analog circuits, and IGBT power device driving circuits.

MODEL SELECTION

H0505S-2W



PRODUCT PROGRAM

Part Number	Input		Output			Efficiency (% Typ)		
	Voltage (VDC)		Voltage (VDC)	Current (mA)				
	Nominal	Range		Max	Min			
G0505S-2W *	5	4.5-5.5	±5	±200	±20	74		
G0509S-2W *			±9	±111	±12	77		
G0512S-2W *			±12	±83	±9	77		
G0515S-2W *			±15	±67	±7	77		
H0505S-2W			5	400	40	74		
H0509S-2W			9	222	23	77		
H0512S-2W			12	167	17	77		
H0515S-2W			15	133	14	77		
G1205S-2W			12	10.8-13.2	±5	±200	±20	75
G1209S-2W *					±9	±111	±12	78
G1212S-2W *	±12	±83			±9	80		
G1215S-2W *	±15	±67			±7	78		
H1205S-2W	5	400			40	75		
H1209S-2W	9	222			23	78		
H1212S-2W	12	167			17	80		
H1215S-2W	15	133			14	78		
G2405S-2W *	24	21.6-26.4			±5	±200	±20	75
G2409S-2W *					±9	±111	±12	77
G2412S-2W *			±12	±83	±9	80		
G2415S-2W *			±15	±67	±7	79		
H2405S-2W			5	400	40	75		
H2409S-2W			9	222	23	77		
H2412S-2W			12	167	17	80		
H2415S-2W			15	133	14	79		

* Designing.

OUTPUT SPECIFICATIONS

Item	Test Conditions	Min	Typ	Max	Units
Output power		0.2		2	W
Line regulation	For Vin change of $\pm 1\%$			± 1.2	
Load regulation	10% to 100% load(5V output)		10	15	%
	10% to 100% load(9V output)		8.3	15	
	10% to 100% load(12V output)		6.8	15	
	10% to 100% load(15V output)		6.3	15	
Output voltage accuracy		See tolerance envelope graph			
Temperature drift	100% full load			0.03	$\% / ^\circ\text{C}$
Ripple & Noise*	20MHz Bandwidth		150	250	mVp-p
Switching frequency	Full load nominal input (5V input)		35		KHz
	Full load nominal input (12V/24V input)		50		

*Test ripple and noise by "parallel cable" method. See detailed operation instructions at Testing of Power Converter section, application notes.

Note:

- All specifications measured at TA=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
- See below recommended circuits for more details.

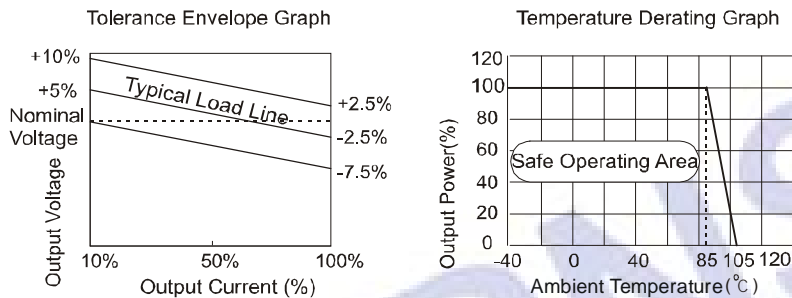
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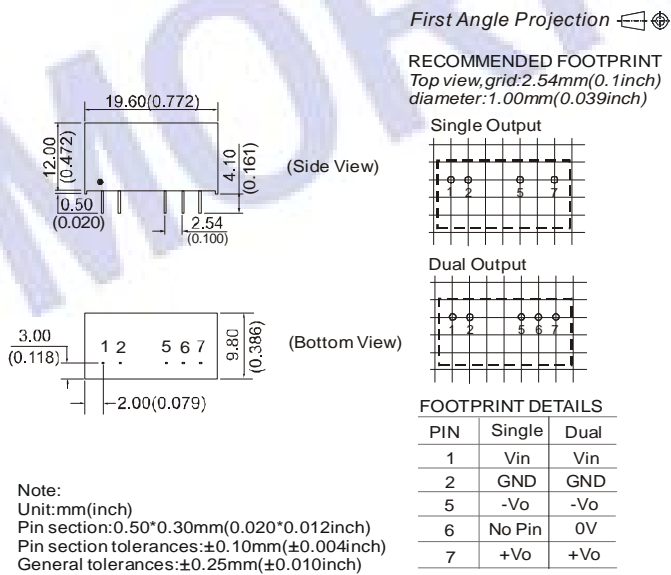
ISOLATION SPECIFICATIONS					
Item	Test Conditions	Min	Typ	Max	Units
Isolation voltage	Tested for 1 minute and 1mA max	6000			VDC
Isolation resistance	Test at 500VDC	1000			MΩ
Isolation capacitance				10	pF

COMMON SPECIFICATIONS					
Item	Test conditions	Min	Typ	Max	Units
Storage humidity				95	%
Operating temperature		-40		85	°C
Storage temperature		-55		125	
Lead temperature	1.5mm from case for 10 seconds			300	
Temp. rise at full load			15	30	
Short circuit protection		Continuous			
Cooling		Free air convection			
Case material		Plastic(UL94-V0)			
MTBF		3500			K hours
Weight			4.3		g

TYPICAL CHARACTERISTICS



OUTLINE DIMENSIONS & PIN CONNECTIONS



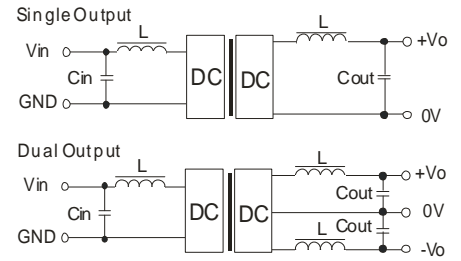
APPLICATION NOTE

Requirement on output load

To ensure this module can operate efficiently and reliably, During operation, the minimum output load is **not less than 10%** of the full load, and that **this product should never be operated under no load!** If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load; or use our company's products with a lower rated output power (G_S-1W & H_S-1W).

Recommended testing and application circuit

If you want to further decrease the input/output ripple, an "LC" filtering network may be connected to the input and output ends of the DC/DC converter, see (Figure 1).



(Figure 1)

It should also be noted that the inductance and the frequency of the "LC" filtering network should be staggered with the DC/DC frequency to avoid mutual interference. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor sees (Table 1).

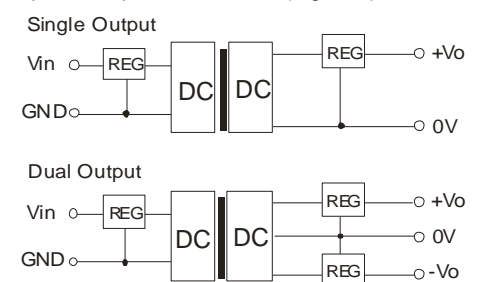
EXTERNAL CAPACITOR TABLE (Table 1)

Vin (VDC)	Cin (uF)	Single Vout (VDC)	Cout (uF)	Dual Vout (VDC)	Cout (uF)
5	10	5	10	±5	4.7
12	4.7	9	4.7	±9	2.2
24	2.2	12	2.2	±12	1
-	-	15	1	±15	0.47

It's not recommend to connect any external capacitor in the application field with less than 0.5 watt output.

Output Voltage Regulation and Over-voltage Protection Circuit

The simplest device for output voltage regulation, over-voltage and over-current protection is a linear voltage regulator with overheat protection that is connected to the input or output end in series (Figure 2).



(Figure 2)

Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against overload. The simplest method is to connect a self-recovery fuse in series at the input end or add a circuit breaker to the circuit.

No parallel connection or plug and play.